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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE PATENT OF:

Date: 5/16/2005

Phillip Celaya et al.

Group: 2827

PATENT NO.: 6889429 *B2*  
ISSUED: 5/10/2005

Certificate  
MAY 24 2005  
of Correction

FOR: METHOD OF MAKING A LEAD-FREE INTEGRATED CIRCUIT PACKAGE

REQUEST UNDER 35 U.S.C. 254  
REQUEST FOR CERTIFICATE OF CORRECTION OF OFFICE MISTAKE

HON. COMMISSIONER OF PATENTS & TRADEMARKS  
WASHINGTON, D.C. 20231

S I R :

In accordance with 37 CFR 1.322, Applicants hereby request the Commissioner of Patents and Trademarks to make correction of the patent identified above.

In claim 16, last line, the Office omitted the number '50' in the distance range of microns. Claim 16 corresponds to claim 36 of amendment filed December 16, 2004, which was allowed by the Examiner. Applicants therefore request that claim 16, column 6, line 23, be corrected to read "about 50 microns and about 125 microns."

It is respectfully stated this error was not due to the fault of the applicants and therefore no fee is due. Certificate of Correction form PTO/SB/44 is enclosed in triplicate.

Please address any inquiries with respect to this matter to Kevin B. Jackson at 602 244 4885.

Respectfully submitted,

Phillip Celaya et al.

ON Semiconductor  
Intellectual Property Dept.  
P.O. Box 62890 - A700  
Phoenix, AZ 85082-2890

Kevin B. Jackson  
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MAY 25 2005

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(Also Form PTO-1050)

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,889,429 *B2*  
DATED : May 10, 200  
INVENTOR(S) : Phillip C. Celaya et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In claim 16, column 6, line 23, insert --50-- after "about"

MAILING ADDRESS OF SENDER: Semiconductor Components Industries, L.L.C.  
P.O. Box 62890 (A700)  
Phoenix, AZ 85082-2890

PATENT NO. 6,889,429

No. of additional copies



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**MAY 25 2005**